Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims

1. (Currently Amended) Process for displaying data on a matrix display having N data lines and P selection lines at the intersections of which are situated the image points or pixels, and in which the N data lines are grouped into P blocks of N' data lines each (N = P x N'), each block receiving in parallel one of the P data signals which is demultiplexed on the N' lines of said block, wherein, alternately, according to the selection lines, the scanning of the N' data lines of a block is carried out from 1 to N' and from N' to 1, wherein a scanning direction is controlled as a function of the selection line to provide a temporal average of a column.

2. (Previously Presented) Process according to Claim 1, wherein the scan from 1 to N' then from N' to 1 is carried out every second selection line, the scan being carried out in a first direction for a first selection line and in a second direction for a succeeding selection line.

3. (Original) Process according to Claim 1, characterized in that the scan from 1 to N' then from N' to 1 is carried out for four successive selection lines, the scan being carried out in a first direction for two successive selection lines and in a second direction for the other two succeeding selection lines.

4. (Original) Circuit for implementing the process according to any one of Claims 1 to 3, characterized in that it consists of at least one programmable logic circuit associated with a line counter determining the reversal of the direction of scan.